

Docket No.: 20433-00601-US2
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Katsuki Hazama

Application No.: To be assigned
(Cont. of 10/118,039)

Art Unit: To be assigned

Filed: Concurrently herewith

Examiner: To be assigned

For: SEMICONDUCTOR DEVICE INCLUDING
TRANSISTOR WITH COMPOSITE GATE
STRUCTURE AND TRANSISTOR WITH
SINGLE GATE STRUCTURE, AND METHOD
FOR MANUFACTURING THE SAME

FIRST PRELIMINARY AMENDMENT

MS Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

Prior to examination on the merits, please amend the above-identified U.S. patent application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 9 of this paper.